



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ARCH D. ROBISON

Application No.: 09/844,345

Filed: April 27, 2001

For: **PRUNING LOCAL GRAPHS IN AN  
INTER-PROCEDURAL ANALYSIS  
SOLVER**

Art Group: 2122

Examiner:

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**APR 12 2002**

**Technology Center 2100**

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Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Copies of the references cited on PTO/SB/08 are enclosed herewith.

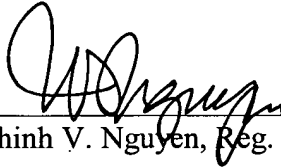
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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



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Date: April 04, 2002

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 to the Patent and Trademark Office on: April 04, 2002.

  
**Barbara Hayashi**

04/04/02

**Date**

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42P10802

Substitute for Form 1449A/PTO (Modified)		Complete if Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Application Number	09/844,345
		Filing Date	4/27/01
		First Named Inventor:	Robison
		Group Art Unit	2122
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	042390.P10802	Technology Center 2100
OTHER AND NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		COUSOT, P., and COUSOT, R., Abstract Interpretation: A Unified Model for Static Analysis of Programs by Construction or Approximation of Fixpoints, In Conference Record of the Fourth Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, (Los Angeles, CA, 1977), ACM Press, 238-252	
		Tool Interface Standards, <i>ELF: Executable and Linkable Format</i> , <a href="ftp://ftp.intel.com/tis/elf11g.zip">ftp://ftp.intel.com/tis/elf11g.zip</a>	
		Unix International, Programming Languages SIG. <i>Dwarf Debugging Information Format</i> , Revision 2.0.0 (July 27, 1993), <a href="ftp://ftp.intel.com/pub/tis/dwf11.zip">ftp://ftp.intel.com/pub/tis/dwf11.zip</a>	
		CHENG, B.C., and HWU, W.M., Modular Interprocedural Pointer Analysis Using Access Paths: Design Implementation and Evaluation. In <i>Proceedings of the SIGPLAN '00 Conference on Programming Language Design and Implementation</i> (Vancouver, British Columbia, June 2000), ACM Press, 57-69.	
		Edison Design Group, <i>C++ Front End, Internal Documentation (excerpt)</i> , (August 2000), <a href="http://www.edg.com/docs/edg_cpp.pdf">http://www.edg.com/docs/edg_cpp.pdf</a>	
		COOPER, K.D., and KENNEDY, K., and TORCZON, L., Interprocedural Optimization: Eliminating Unnecessary Recompilation. In <i>Proceedings of SIGPLAN '86 Symposium on Compiler Construction</i> (Palo Alto, CA, June 1986) ACM Press 58-67.	
		BURKE, M., and TORCZON, L., Interprocedural Optimization: Eliminating Unnecessary Recompilation. In <i>ACM Transactions on Programming Languages and Systems</i> , Vol. 15(3), 367-399	
		HALL, M.W., HIRANANDANI, S., KENNEDY, K., and TSENG, C.W., Interprocedural Compilation of Fortran D for MIMD Distributed-Memory Machines, <i>Supercomputing '92</i> , 522-534	
		POLLOCK, L.L., and SOFFIA, M.L., Incremental Global Reoptimization of Programs. In <i>ACM Transactions on Programming Languages and Systems</i> , Vol. 14(2), 173-200	
		GOUGH, K.J., and KLAEREN, H., Executable Assertions and Separate Compilation. In <i>proceeding of 1997 Joint Modular Languages Conference</i> (Linz, Austria) Lecture Notes in Computer Science, Vol. 1204, 41-52	

Examiner Signature	Date Considered
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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